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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete if Known	
		Application Number	
		Filing Date	
		First Named Inventor	NADEAU-DOSTIE, Benoi
		Group Art Unit	
		Examiner Name	
Sheet 2	of 2	Attorney Docket Number	LVPAT066US

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JK		CHEN et al., "Enabling Embedded Memory Diagnosis via Test Response Compression", 19th IEEE VLSI Test Symposium (VTS 2001).	
		SCHANSTRA et al., "Semiconductor Manufacturing Process Monitoring using Built-In Self-Test for embedded Memories, 1998 ITC Proceedings.	
		CHEN et al., "Test Response Compression and Bitmap Encoding for Embedded Memories in Manufacturing Process Monitoring", 2001 ITC Proceedings. p. 258.	

Examiner Signature	/James Kerveros/	Date Considered	03/02/2007
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**INFORMATION DISCLOSURE
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Sheet 1 of 2

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Application Number	10/690,594
Filing Date	October 23, 2003
First Named Inventor	NADEAU-DOSTIE, Benoît
Group Art Unit	2811
Examiner Name	N/A
Attorney Docket Number	LVPAT066US

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Sheet	2	of	2
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Application Number	10/690,594
Filing Date	October 23, 2003
First Named Inventor	NADEAU-DOSTIE, Benoit
Group Art Unit	2811
Examiner Name	N / A
Attorney Docket Number	LVPAT066US

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